EUROPEAN PATENT OFFICE

Patent Abstracts of Japan

PUBLICATION NUMBER

06291039

PUBLICATION DATE

18-10-94

APPLICATION DATE

06-04-93

APPLICATION NUMBER

APPLICANT: SANYO ELECTRIC CO LTD:

05105003

INVENTOR : HARADA YASUKI;

INT.CL.

: H01L 21/20 H01L 21/268 H01L 21/314

H01L 29/784 H01L 21/336

TITLE

: AMORPHOUS-SEMICONDUCTOR FORMATION SUBSTRATE AND

MANUFACTURE OF

POLYCRYSTALLINE-SEMICONDUCTOR FORMATION SUBSTRATE USING IT

25:テンシル裏 2a:テンシル数 1:低職点ガラス基板

ABSTRACT :

PURPOSE: To provide an amorphous semiconductor formation substrate whose film thickness is comparatively thick so as not to thermally damage a low-melting-point glass substrate and which is provided with a buffet layer not increasing an internal stress due to the thick film thickness.

CONSTITUTION: An amorphous semiconductor formation substrate is formed in such a way that an a-Si film 5 is formed on a low-melting-point glass substrate 1 via a buffer layer 4 for impurity-diffusion prevention. In the formation substrate, the buffer layer 4 is formed of a film in which tensile films 2a to 2c composed of SiO2 and compressive films 3a to 3c

composed of SiO₂ in the same manner have been laminated alternately.

COPYRIGHT: (C) JPO